**PATENT** 

## **DOCKET NO. 321546US**

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Simon Knowles

Serial No.:

10/813,433

Filed:

March 31, 2004

For:

APPARATUS AND METHOD FOR DUAL DATA PATH PROCESSING

Group No.:

2183

Examiner:

David J. Huisman

Confirmation No.:

3801

Sir:

## **INFORMATION DISCLOSURE STATEMENT**

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicants submits this statement. This submittal is made in accordance with 37 C.F.R. §1.97 and §1.98 and §609 of the Manual of Patent Examining Procedure. The patents, publications, and other information herein are listed below and on the attached Form PTO-1449.

Reference not in English Language

The publication listed on attached Form PTO-1449, page 457 of the Chinese textbook

entitled "Computer Organization and Architecture - Designing for Performance", Fifth Edition, May

2001, was cited in the Chinese prosecution of the above noted application. The Examiner in the

Chinese prosecution cited this reference with regard to dependent Claims 7-8 to assert a teaching that

a plurality of fixed operators of a first data execution path are arranged to perform a plurality of fixed

operations in independent lanes according to single instruction, multiple data principles and that a

plurality of configurable operators of a second data execution path are arranged to perform multiple

operations in different lanes according to single instruction, multiple data principles.

The Commissioner is hereby authorized to charge any additional fees which may be required,

or credit any overpayment to Deposit Account No. 08-2395.

Respectfully submitted,

Steve J. Ha

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Date: July 29, 2010

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